

AMENDMENTS TO THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended) A computer system comprising:
a first video controller;
a second video controller; and
a switching device configured to receive a first signal from the first video controller and a second signal from the second video controller, and configured to provide the first signal or the second signal to a first display device;
a chipset that includes the first video controller and the switching device,
wherein the second video controller is coupled to the chipset;
the switching device being coupled to a first connector configured to receive the first display device; and
the switching device being coupled to a second connector configured to receive a second display device.
2. (Cancelled).
3. (Cancelled).
4. (Currently Amended) The computer system of claim 31, wherein the chipset includes an AGP port, and wherein the second video controller is configured to provide the second signal to the switching device using the AGP port.
5. (Cancelled).

6. (Currently Amended) The computer system of claim 21, further comprising:
a processor coupled to the chipset; and
a system memory configured to store a program that is executable by the processor;
wherein the program includes instructions for causing the switching device to provide the first signal or the second signal to the first display device.
7. (Cancelled).
8. (Cancelled).
9. (Currently Amended) The computer system of claim 81, wherein the switching device is configured to provide the first signal or the second signal to the second display device.
10. (Currently Amended) A computer system comprising:
a first video controller;
an interface configured to receive a second video controller; and
a switching device coupled to the first video controller and the interface;
wherein the switching device is configured to provide a first signal from the first video controller to a first display device in response to the second video controller not being coupled to the interface, and wherein the switching device is configured to provide a second signal from the second video controller to the first display device in response to the second video controller being coupled to the interface;
a chipset including the first video controller and the switching device, the interface being coupled to the chipset;
the switching device being coupled to a first connector configured to

receive the first display device; and
the switching device being coupled to a second connector configured to
receive a second display device.

11. (Cancelled).
12. (Cancelled).
13. (Currently Amended) The computer system of claim 4210, wherein the chipset includes an AGP port, and wherein the AGP port is configured to receive the second signal from the second video controller.
14. (Cancelled).
15. (Currently Amended) The computer system of claim 4410, further comprising:
a processor coupled to the chipset; and
a system memory configured to store a program that is executable by the processor;
wherein the program includes instructions for causing the switching device to provide the first signal from the first video controller to the first display device in response to the second video controller not being coupled to the interface, and wherein the program includes instructions for causing the switching device to provide the second signal from the second video controller to the first display device in response to the second video controller being coupled to the interface.
16. (Cancelled).
17. (Cancelled).

18. (Currently Amended) The computer system of claim 17, wherein the switching device is configured to provide the first signal or the second signal to the second display device.

19. (Cancelled).

20. (Cancelled).

21. (Currently Amended) A method of providing a video signal to a display device in a scalable platform comprising:

providing a first video controller;

configuring an interface to receive a second video controller;

coupling a switching device to the first video controller and the interface;

configuring the switching device to provide a first signal from the first video controller to a first display device in response to the second video controller not being coupled to the interface; and

configuring the switching device to provide a second signal from the second video controller to the first display device in response to the second video controller being coupled to the interface;

providing a chipset including the first video controller and the switching device, the interface being coupled to the chipset;

coupling the switching device to a first connector configured to receive the first display device; and

coupling the switching device to a second connector configured to receive a second display device,

whereby the switching device is configured to provide either of the first and second signals to the second display device.